



DEPARTMENT OF E&TC ENGINEERING

TIME TABLE

CLASS: TE (VLSI) AY 2025-26 SEM I W.E.F :-23-07-2025

TIME	08:15 AM to	09:15 AM to	10:15 AM to	10:30 AM to	11:30 AM to	11:30 AM to	12:30 PM to	01:00 PM to	02:00 PM to	3:00 PM to	
Day	09:15 AM	10:15 AM	10:30 AM	11:30 AM	12:30 PM	12:30 PM	1:00 PM	02:00 PM	03:00 PM	4:00 PM	
Monday	CN-A-431-VVD		SHORT BREAK	MC	CN/DFT	LUNCH BREAK	CA	SELF			
	SD-B-402A-CDR			PPV	VVD/SSN		CDR	LEARNING			
	ADCD-C-452-GDS			414	414/404		417				
Tuesday	MC-A-453-PPV			MC	DC		SD-A-456-CDR		MENTOR MEETING		
	ADCD-D-452-GDS			PPV	VSN		DFT-C-452-SSN				
	CN-B-431-VVD			414	414		DC-D-451-VSN				
	DC-C-451-VSN										
Wednesday	ADCD GDS 425	CA CDR 425			CN/DFT VVD/SSN 404/425		MC PPV 425		DC VSN 425	LIBRARY HOUR	
Thursday	ADCD-B-452-GDS			DC	CN/DFT			ADCD	CA	DEPT MEETING	
	MC-C-453-PPV			VSN	VVD/SSN			GDS	CDR		
				425	425/404			417	417		
	SD-D-402A-CDR										
Friday	DC-A-451-VSN			ADCD-A-452-GDS				ADCD GDS 425		GFM MEETING	
	MC-B-453-PPV			DC-B-451-VSN							
	DFT-D-452-SSN			SD-C-402A-CDR							
			MC-D-453-PPV								

Subject:

DC DIGITAL COMMUNICATION
CA COMPUTER ARCHITECTURE
ADCD ANALOG & DIGITAL CMOS DESIGN
MC MICROCONTROLLERS
CN COMPUTER NETWORKS
DFT DESIGN FOR TESTABILITY
SD SKILL DEVELOPMENT

Faculty

VSN
CDR
GDS
PPV
VVD
SSN

MS V S NAVALE
MS C D RANANAWARE
DR G D SALUNKE
DR P P VAST
DR V V DESHMUKH
DR S S NIKAM

MS A A RANDIVE
Time-Table Incharge

Dr S B DHONDE
Head E&TC