



AISSMS

COLLEGE OF ENGINEERING

ज्ञानम् सकलजगद्दिताय

Accredited by NAAC with "A+" Grade | NBA - 7 UG Programmes



DEPARTMENT OF E&TC ENGINEERING-VLSI DT

TIME TABLE

CLASS: SE (VLSI)

AY 2025-26 SEM II

W.E.F :-27-01-2026

TIME Day	08:15 AM to 09:15 AM	09:15 AM to 10:15 AM	10:15 AM to 10:30 AM	10:30 AM to 11:30 AM	11:30 AM to 12:30 PM	12:30 PM to 1:00 PM	01:00 PM to 02:00 PM	02:00 PM to 03:00 PM	3:00 PM to 4:00 PM
Monday	EA RRI 425	FPGA VDN 425	SHORT BREAK	SS OOP-A-431-CDR/SBD1		LUNCH BREAK	CE VSN 414	SS CDR 414	SELF LEARNING
				FPGA-B-452-VDN					
				ESD-C-432-VBG					
Tuesday	FPGA VDN 414	EPSD VBG 414		SS OOP-B-431-CDR/SBD1			SS CDR 425	CE VSN 425	MENTOR MEETING
				FPGA-C-452-VDN					
				ESD-D-432-VBG					
Wednesday	OOP SBD1 414	ESD(TUT) SSN 414		ESD-A-432-VBG			MIL YPL 414	MIL YPL 414	
				SS OOP-C-431-CDR/SBD1					
				FPGA-D-452-VDN					
Thursday	EA RRI 414	FPGA VDN 414		FPGA-A-452-VDN			OOP SBD1 414	LIBRARY HOUR	DEPT MEETING
				ESD-B-432-VBG					
				SS OOP-D-431-CDR/SBD1					
Friday	SS CDR 414	CE VSN 414		OE-II SHL/NPM 425/414	OE-II SHL/NPM 425/414		OOP SBD1 425	EPSD VBG 425	GFM MEETING

Subject:

CE	COMMUNICATION ENGINEERING
SS	SIGNALS & SYSTEMS
FPGA	FPGA BASED SYSTEM DESIGN
OOP	OBJECT- ORIENTED PROGRAMMING
ESD	ELECTRONICS SKILL DEVELOPMENT
MIL	MODERN INDIAN LANGUAGES
EPSD	ENTREPRENEURSHIP SKILL DEVELOPMENT
OE-II	OPEN ELECTIVE-II
EA	ENVIRONMENT AWARENESS

Faculty:

VSN	Mrs.V.S.NAVALE
CDR	Mrs.C.D.RANANAWARE
VDN	Ms.V.D.NAGRALE
SBD1	Mr.S.B.DHEKALE
VBG	Mr.V.B.GAWAI
SSN	Dr.S.S.NIKAM
YPL	Mrs.Y.P.LAD
SHL	Dr.S.H.LAVATE
NPM	Mr.N.P.MAWALE
RRI	Dr.R.R.ITKARKAR

Mrs.A.A.RANDIVE
Dept. Time-Table I/C

Mr.S.B.DHEKALE
DAC

Dr.S.B.DHONDE
Head E&TC

Mr.P.S.TADKAR
Central Time-Table I/C

Dr.D.Y.DHANDE
Dean Academics