



AISSMS

COLLEGE OF ENGINEERING
ज्ञानम् सफलतामहिताय

Accredited by NAAC with "A+" Grade | NBA - 7 UG Programmes



DEPARTMENT OF E&TC ENGINEERING-VLSI DT

AY 2025-26 SEM II

W.E.F :-26-02-2026

TIME												
Day	CLASS	08:15 AM to 09:15 AM	09:15 AM to 10:15 AM	10:15 AM to 10:30 AM	10:30 AM to 11:30 AM	11:30 AM to 12:30 PM	12:30 PM to 1:00 PM	01:00 PM to 02:00 PM	02:00 PM to 03:00 PM	3:00 PM to 4:00 PM		
MON	S E	EA RRI 425	FPGA VDN 425	SHORT BREAK	SS OOP-A-431-CDR/SBD1 FPGA-B-452-VDN ESD-C-432-VBG		LUNCH BREAK	CE VSN 414	SS CDR 414	SELF LEARNING		
	T E		LIBRARY HOUR		EP PPV 317	ASICD/NS SSN/SOA 317/306		EP-A-453-PPV MP-B-402A ASICD-C-452-SSN SVL-D-456-NPM		ERFC-A- VVD 414		
	Tue	S E	FPGA VDN 414		EPSD VBG 414	SS OOP-B-431-CDR/SBD1 FPGA-C-452-VDN ESD-D-432-VBG		SS CDR 425	CE VSN 425	MENTOR MEETING		
		T E	CITP ACTIVITY		EP PPV 317	ASICD/NS SSN/SOA 317/456		ERFC VVD 317	SVL-A-456-NPM EP-B-453-PPV MP-C-402A ASICD-D-452-SSN		PM GDS 425	
Wed		S E	OOP SBD1 414		ESD(TUT) SSN 414	ESD-A-432-VBG SS OOP-C-431-CDR/SBD1 FPGA-D-452-VDN		MIL YPL 414	MIL YPL 414			
		T E	ERFC-B- VVD 432		ERFC-C- VVD 432	EP PPV 425		ERFC VVD 425	PM GDS 425	ASICD/NS SSN/SOA 425/456	ERFC-D- VVD 414	
	Thu	S E	EA RRI 414		FPGA VDN 414	FPGA-A-452-VDN ESD-B-432-VBG SS OOP-D-431-CDR/SBD1		OOP SBD1 414	LIBRARY HOUR	DEPT MEETING		
		T E			SELF LEARNING	ERFC VVD 414		EP PPV 414	NS-A-431-SOA SVL-B-456-VDN EP-C-453-PPV MP-D-402A		PM GDS 317	
Fri		S E	SS CDR 414		CE VSN 414	OE-II SHL/NPM 425/414		OE-II SHL/NPM 425/414	OOP SBD1 425	EPSD VBG 425	GFM MEETING	
		T E	MP-A-402A ASICD-B-452-SSN SVL-C-456-VDN EP-D-453-PPV			PM GDS 317		ERFC VVD 317	ASICD/NS SSN/SOA 317/456	MP(A,B,C,D)		

Subject SE

CE COMMUNICATION ENGINEERING
SS SIGNALS & SYSTEMS
FPGA FPGA BASED SYSTEM DESIGN
OOP OBJECT- ORIENTED PROGRAMMING
ESD ELECTRONICS SKILL DEVELOPMENT
MIL MODERN INDIAN LANGUAGES
EPD ENTREPRENEURSHIP SKILL DEVELOPMENT
OE-II OPEN ELECTIVE-II
EA ENVIRONMENT AWARENESS

Subject: TE

ERFC ELECTROMAGNETICS & READIO FREQUENCY CIRCUITS
PM PROJECT MANAGEMENT
EP EMBEDDED PROCESSORS
ASICD ASIC DESIGN
SOA NETWORK SECURITY
SVL SYSTEM VERILOG LAB
MP MINI PROJECT

Mrs.A.A.RANDIVE
Dept. Time-Table I/C

Mr.S.B.DHEKALE
DAC

Dr.S.B.DHONDE
Head E&TC

Mr.P.S.TADKAR
Central Time-Table I/C

Dr.D.Y.DHANDE
Dean Academics