



Department of Electronics & Telecommunication Engineering

Faculty Profile



Name:	Miss Vismita Devidas Nagrale		
Designation:	Assistant Professor		
Email Id:	vdnagrale@aissmscoe.com		
Contact No:	020-261054545, 261058587	Extn :	
Educational Qualification:	1. PHD: Pursuing		
	2. ME: Electronics Engineering		
	3. BE: Electronics Engineering		
	4. Post Graduate Diploma in Software Technology		
Experience:	Teaching- 18Yrs	Industry- 2Yrs	Total- 20Yrs
Awards/ Achievements			
Subject Thought (UG &PG)	1. VLSI Design		
	2. Microcontrollers		
	3. Employability Skill Development		
	4. Embedded processors		
	5. Fundamentals of HDL		
	6. Integrated circuits and applications		
	7. Biomedical Signal Processing		

Course/ Seminar/Workshop/FDP Attended:	
1. 1 Week STTP on “ Matlab Programming,” at VIIT Pune.	
2. 1 Week STTP on “ Emerging Research Trends in Electronics, Telecommunication and allied fields” in Association with IEEE communication society Pune chapter, Renu Electronics Pvt. Ltd Pune and Scienteck Pvt. Ltd.	
3. 1 Week workshop on Hands on Training Using Open Source softwares for Signal Processing and Building Embedded Systems	
4. 2 Days workshop on State level seminar on ‘Data Mining and Optimization techniques’	
5. 1 Week FDP orientation for new revised course of VLSI DESIGN	
6. I week FDP on CETL’s “Vision-Mission-Goals-PEO-PSO-PO-CO: Framing, Mapping, Assessment and Attainment” at AISSMS COE, Pune	
7. I week FDP on Data science at AISSMS COE, Pune,	
8. 1 week FDP Orientation Program on Modern Pedagogy and Recent Technological Development	
Area of Specialization:	1. VLSI Design
	2. Biomedical Engineering
	3. Embedded System
Publication Details(Journal/Conference):	1. Image Stegano-Cryptography Based on LSB Insertion & Symmetric Key Encryption Authors Vismata Nagrale, Ganesh Zambre, Aamir Agwani
	2. Embedded Web Server Based Automation Authors JA Nanajkar, DN Vismata
	3. Catercam-A Sliding Camera Mechanism Using Gimbal Assembly Authors K Kirtikar, S Ghodke, A Shirole, V Nagrale
	4. Robust adaptive beamforming with positive semi-definite constraint using single variable minimization Authors SP Patil, VD Nagrale
	5. Implementation Of Matrix Keyboard In Digital X-Ray Machine By Using Embedded System Design Authors V Nagrale, RN Moghe, S Kanse
	6. A Review on Design of a high gain MicrostripAntenna using Parasitic Patch for GPS Application Authors Rupesh Budharam Raut, Vismata. Nagrale

Patent/IPR/Research Work:	1. Nil
Responsibility Handled:	1. NAAC Criteria incharge of IIB
	2. Member of College Development Committee (CDC)
	3. CITP coordinator, Alumni coordinator, industry institute interaction etc
	4. Coordinator of Centre of Excellence Anywhere Automation
Guest Lecture/Seminar Delivered:	1. Resource person for Mechatronics subject in production sandwich engineering on Microcontrollers and applications
No of Projects Supervised	UG:20
	PG: 6
	PHD: Nil